IFW



OIPE IAP72 DEC 9 1 2005 PATENT & TRADEMARK OFFICE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In application of:** Qadeer et al.
**Application No.** 10/765,717
**Filed:** January 26, 2004
**Confirmation No.** 2565
**For:** DATA RACE DETECTION USING SEQUENTIAL PROGRAM ANALYSIS
**Examiner:** Not yet assigned
**Art Unit:** 2185
**Attorney Reference No.** 3382-66931-01

**CERTIFICATE OF MAILING**

I hereby certify that this paper and the documents referred to as being attached or enclosed herewith are being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450 on the date shown below.

Attorney or Agent for Applicant(s) /Edward J. Wright/

Date Mailed December 15, 2005

**INFORMATION DISCLOSURE STATEMENT**
**PURSUANT TO 37 C.F.R. § 1.97(b)(3)**

COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

Listed on the accompanying form PTO-1449 and enclosed herewith is an English-language document. Applicants respectfully request that this document be listed as a reference cited on the issued patent.

Copies of United States patents and United States published patent applications do not have to be provided to the Patent Office (37 C.F.R. 1.98(a)(2)(ii)). Copies of unpublished U.S. applications do not have to be provided, as long as the application is available on PAIR, as this requirement of 37 C.F.R. § 1.98(a)(2)(iii) has been waived by the United States Patent and Trademark Office pursuant to the Official Gazette Notice on October 19, 2004 (1287 OG 163). Applicants will provide copies of such patents or applications upon request.

Applicants filed this Information Disclosure Statement ("IDS") before the mailing date of a first Office action on the merits. As a result, no fee should be required to file this IDS. However, if the Patent Office determines that a fee is required for Applicants to file this IDS, please charge any such fees, or credit overpayment, to Deposit Account No. 02-4550. A **duplicate** copy of this Information Disclosure Statement is enclosed.

The filing of this IDS shall not be construed to be an admission that the information cited in the statement is, or is considered to be, prior art or otherwise material to patentability as defined in 37 C.F.R. §1.56.

Respectfully submitted,

KLARQUIST SPARKMAN, LLP

One World Trade Center, Suite 1600
121 S.W. Salmon Street
Portland, Oregon 97204
Telephone: (503) 595-5300
Facsimile: (503) 595-5301
cc: Client
Docketing

By _____
Stephen A. Wight
Registration No. 37,759

| | | | |
|---|---|---|---|
| **INFORMATION DISCLOSURE STATEMENT BY APPLICANT** | | **Attorney Docket Number** | 3382-66931-01 |
| | | **Application Number** | 10/765,717 |
| | | **Filing Date** | January 26, 2004 |
| | | **First Named Inventor** | Qadeer |
| | | **Art Unit** | 2185 |
| | | **Examiner Name** | Not yet assigned |



| Examiner's Initials | Cite No. (optional) | OTHER DOCUMENTS |
|---|---|---|
| | | Ball et al., "The SLAM project: debugging system software via static analysis." *Proceedings of the 29th ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages (POPL'02),* ACM Press, 2002, 3 pages. |
| | | Bouajjani et al., "A generic approach to the static analysis of concurrent programs with procedures." *Proceedings of the 30th ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages (POPL'03),* ACM Press, 2003, 12 pages. |
| | | Bush et al., "A static analyzer for finding dynamic programming errors." Software-Practice and Experience, 30(7):775–802, June 2000. |
| | | Corbett et al., "Bandera: extracting finite-state models from Java source code." *International Conference on Software Engineering*, 2000, pages 439–448. |
| | | Das et al., "ESP: Path-sensitive program verification in polynomial time." *Proceedings of the ACM SIGPLAN 2002 Conference on Programming Language Design and Implementation (PLDI'02),* ACM Press, 2002, 12 pages. |
| | | Das, "Unification-based pointer analysis with directional assignments." *Proceedings of the ACM SIGPLAN 2000 Conference on Programming Language Design and Implementation (PLDI'00),* ACM Press, 2000, pages 35–46. |
| | | Engler et al., "Checking system rules using system-specific, programmer-written compiler extensions." *Proceedings of the 4th Symposium on Operating Systems Design and Implementation (OSDI'00),* Usenix Association, 2000, 16 pages. |
| | | Flanagan et al., "Thread-modular model checking." *Proceedings of the SPIN Workshop on Software Verification*, 2003, 15 pages. |
| | | Havelund et al., Model checking Java programs using Java PathFinder." *Software Tools for Technology Transfer (STTT),* 2(4):72–84, 2000. |
| | | Henzinger et al., "Thread-modular abstraction refinement." *CAV 2003: Computer Aided Verification*, 2003, 13 pages. |
| | | Henzinger et al., "Lazy abstraction." *Proceedings of the 29th ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages (POPL'02),* ACM Press, 2002, 13 pages. |
| | | Holzmann, "The Model Checker SPIN." *IEEE Transactions on Software Engineering*, 23(5):279–295, May 1997. |
| | | Reps et al., "Precise interprocedural dataflow analysis via graph reachability." *Proceedings of the 22nd ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages*, ACM, 1995, pages 49–61. |

| | |
|---|---|
| EXAMINER SIGNATURE: | DATE CONSIDERED: |

* Examiner: Initial if reference considered, whether or not in conformance with MPEP 609. Draw line through cite if not in conformance and not considered. Include copy of this form with next communication to applicant.

| INFORMATION DISCLOSURE STATEMENT BY APPLICANT | | | |
|---|---|---|---|
| | | **Attorney Docket Number** | 3382-66931-01 |
| | | **Application Number** | 10/765,717 |
| | | **Filing Date** | January 26, 2004 |
| | | **First Named Inventor** | Qadeer |
| | | **Art Unit** | 2185 |
| | | **Examiner Name** | Not yet assigned |
| **Examiner's Initials*** | **Cite No. (optional)** | **OTHER DOCUMENTS** | |
| | | Robby et al., "Bogor: an extensible and highly-modular software model checking framework." *FSE 2003: Foundations of Software Engineering*, 2003, 10 pages. | |
| | | Sharir et al., "Two approaches to interprocedural data flow analysis." *Program Flow Analysis: Theory and Applications*, Prentice-Hall, 1981, pages 189–233. | |

| EXAMINER SIGNATURE: | DATE CONSIDERED: |
|---|---|

* Examiner: Initial if reference considered, whether or not in conformance with MPEP 609. Draw line through cite if not in conformance and not considered. Include copy of this form with next communication to applicant.